

## Senior Scientist

Ecole/Institution/Société:

**Silicon Austria Labs, Austria / Graz**

Discipline:

**Systems Engineering**

Type d'emploi::

**Full-time**

Date de publication:

**2022-02-23**

Personne à contacter:

**If you wish to apply for this position, please specify that you saw it on AKATECH.tech**

### **Senior Scientist - RFIC Design / mm-Wave and sub-Terahertz Receivers**

#### **YOUR FUTURE RESPONSIBILITIES**

- System & circuit solutions for multi-gigabit wireless chips.
- Involvement in receiver architecture selection & receiver system to circuit specifications translation.
- Implementing blocks & documenting design towards formal design reviews.
- Drive layout & top-level simulations to validate top-level integration.
- Supervision of Junior Scientists.
- Project Responsibilities.

#### **YOUR PROFILE**

- Master's degree in relevant field with 5+ years of experience in related area of expertise or PhD with 3 years of experience.
- Deep understanding of fundamental microwave theories & concepts such as:
  - power waves and scattering parameters
  - transmission-lines concepts
  - power gain expressions
  - gain, noise, and VSWR design trade-offs
- High proficiency in system specification & ability to translate system into circuit requirements at IC level.
- Familiar with various RF receiver architectures & their trade-offs.
- Understanding of mmWave device modeling.
- Insights into packaging effects, supply isolations, high frequency ESD structures & circuit layout for optimum performance.
- Experience in simulation & design of lumped & distributed passive structures.
- Proficient with EDA CAD tools. (Cadence, Mentor, Ansys, etc.)
- Performing Analog Custom Layout.
- Experience in measuring IC performance & debug of design to correlate simulations to measurements.

#### **IMPORTANT FACTS ABOUT SAL**

- This position is endowed with a gross annual salary of € 53.382.- based on the collective agreement for research („Forschungs-Kollektivvertrag“) excluding bonus and depending on

your experience and skills.

- Home Office possible.
- € 4.- /day food allowance in restaurants / € 2.- /day in supermarkets.
- Family- & Kids friendly.
- Free Coffee/Milk/Tee.

## **BECOME PART OF SILICON AUSTRIA LABS**

Silicon Austria Labs is performing cutting-edge research in the course of the 6G Lighthouse on the physical layer of RF systems for future 5G-6G (UE, front and backhaul), and millimeter-wave and sub-Terahertz imaging/-sensing/-radar applications. In this domain, the Research Unit for Millimeter Wave Technologies collaborates closely with university and leading industry partners on [e.g.](#) RF/millimeter-wave transceiver architectures robust in terms of interference and coexistence, reconfigurable RF systems for transceivers and RF/-millimeter-wave front-end modules (architectural reuse and/ or resource sharing), and large-scale phased-array antennas and beamforming structures, which are targeting millimeter-wave and sub-Terahertz communications (5G, 6G), radars and imagers. In support of the design activities the mmWave RF Unit is equipped with the best in-class test infrastructure covering the GHz and THz frequency range.

The top research center for electronic based systems (EBS).

Unfold the future, unfold yourself.

### **Job details**

**Title :** Senior Scientist - RFIC Design / mm-Wave and sub-Terahertz Receivers (f/m/d)

**Employer :** Silicon Austria Labs (SAL)

**Location :** Inffeldgasse 33 Graz, Austria

**Field :** Communication Engineering, Electrical Engineering, Electronics, Engineering, Systems Engineering

Personne à contacter:

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